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FIG.1 (PRIOR ART)

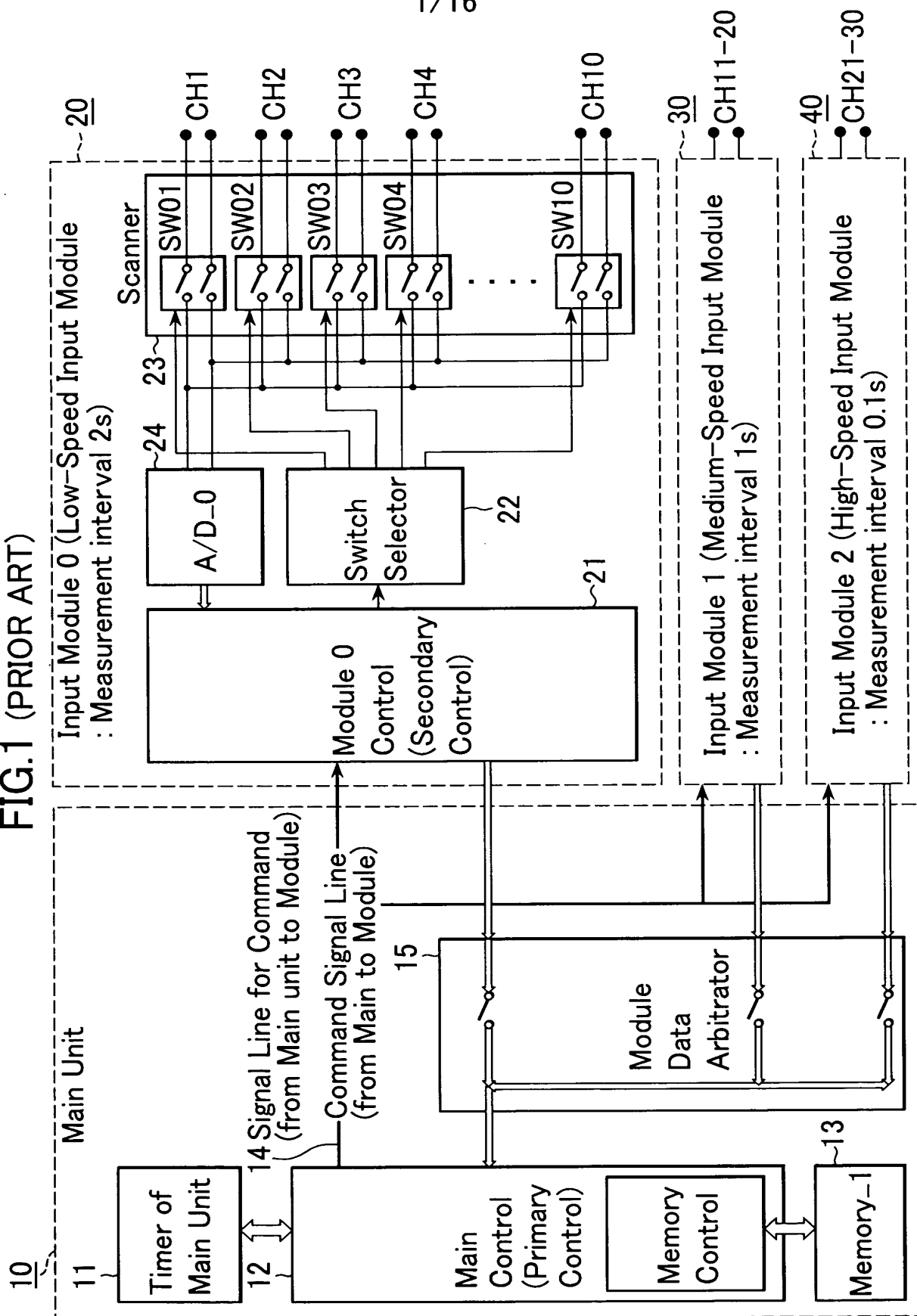
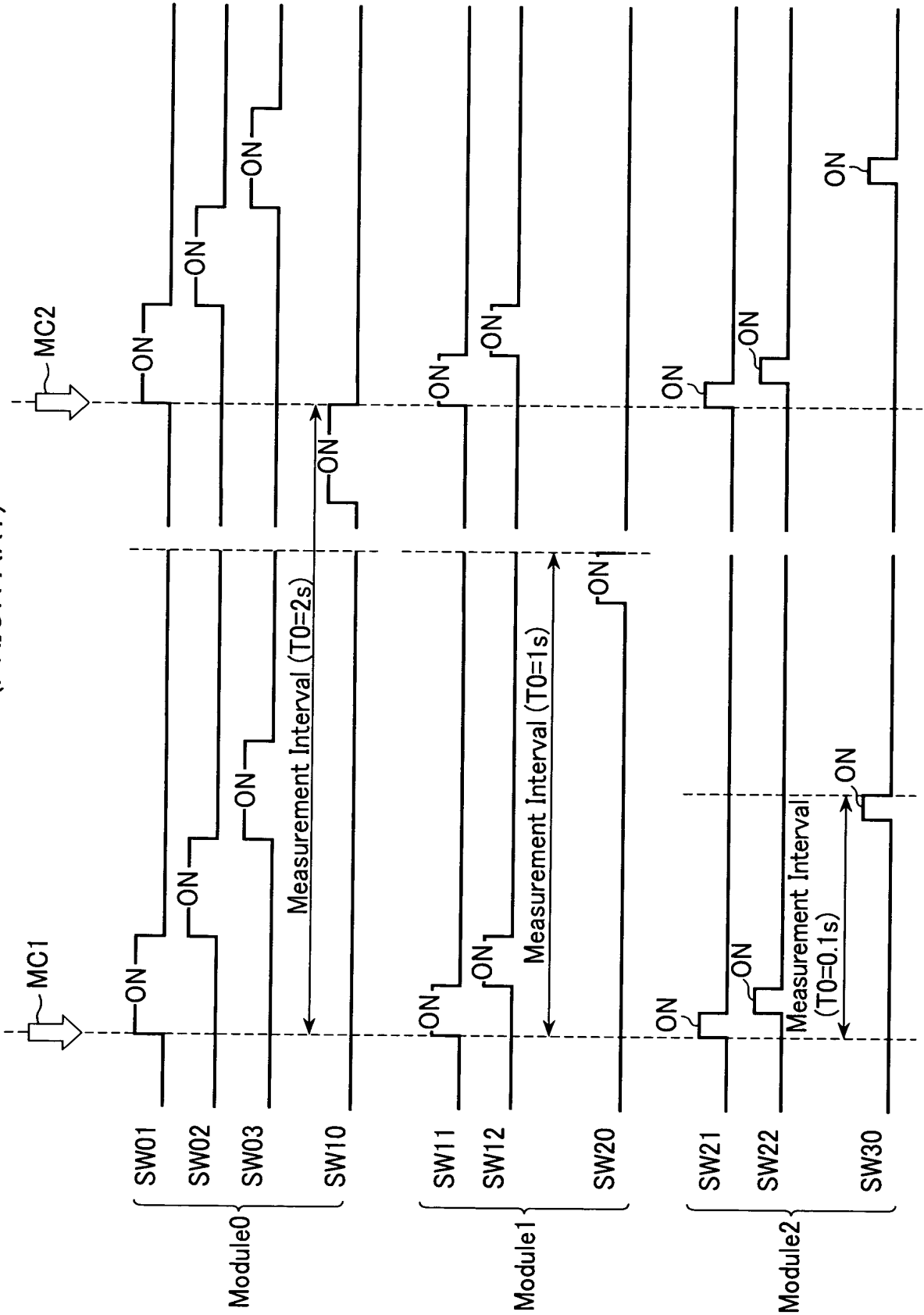
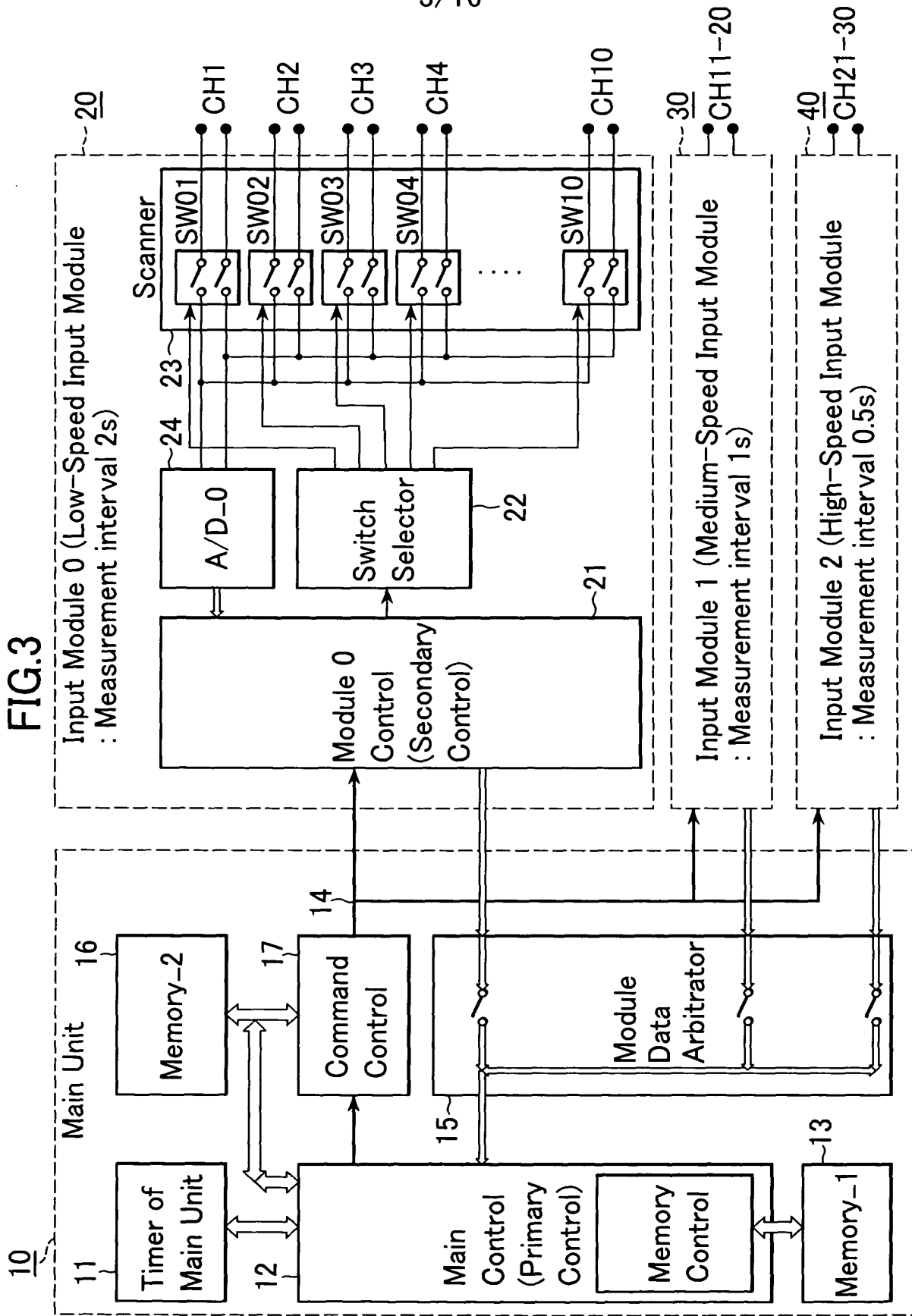


FIG.2 (PRIOR ART)



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FIG.3



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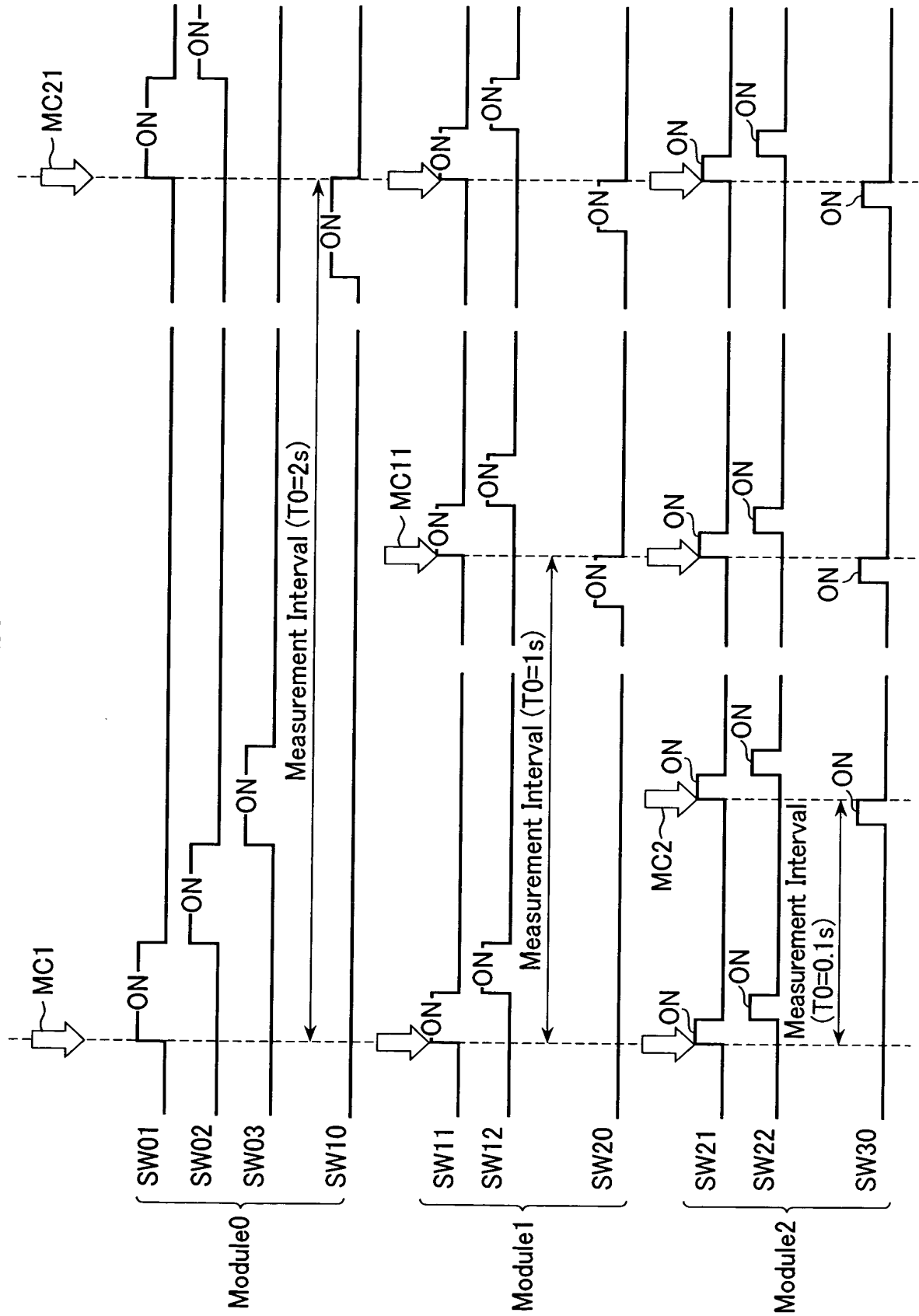
FIG.4

| Measurement<br>Input Module | 1st | 2nd | 3rd | 4th | 5th | 6th | 7th | 8th | 9th | 10th | 11th | 12th | 13th | 14th | 15th | 16th | 17th | 18th | 19th | 20th |
|-----------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|------|------|------|------|------|------|------|------|------|------|------|
| 20                          | 1   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    |
| 30                          | 1   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0    | 1    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    |
| 40                          | 1   | 1   | 1   | 1   | 1   | 1   | 1   | 1   | 1   | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    |

1 : A command is sent  
 0 : A command is not sent

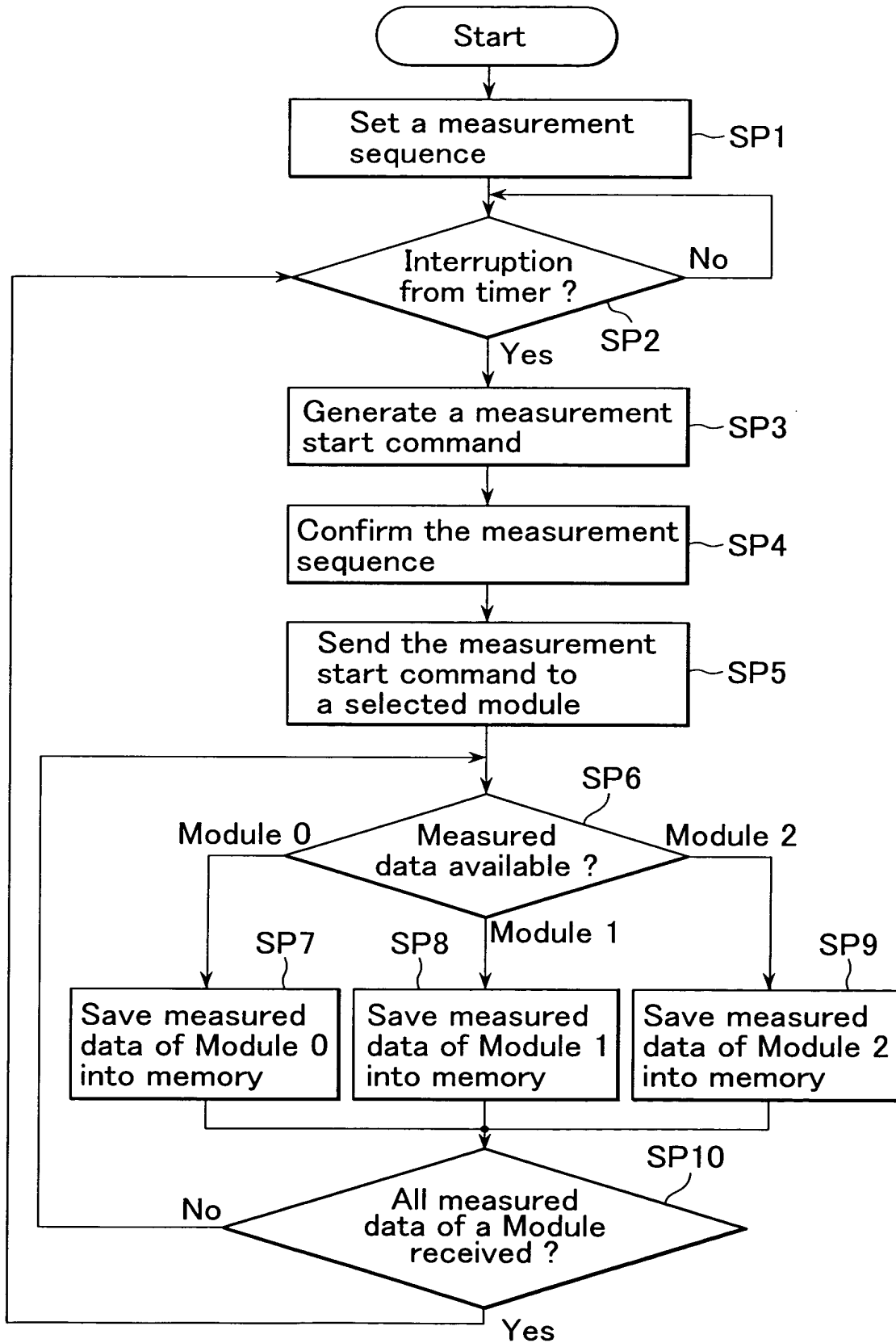
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FIG.5



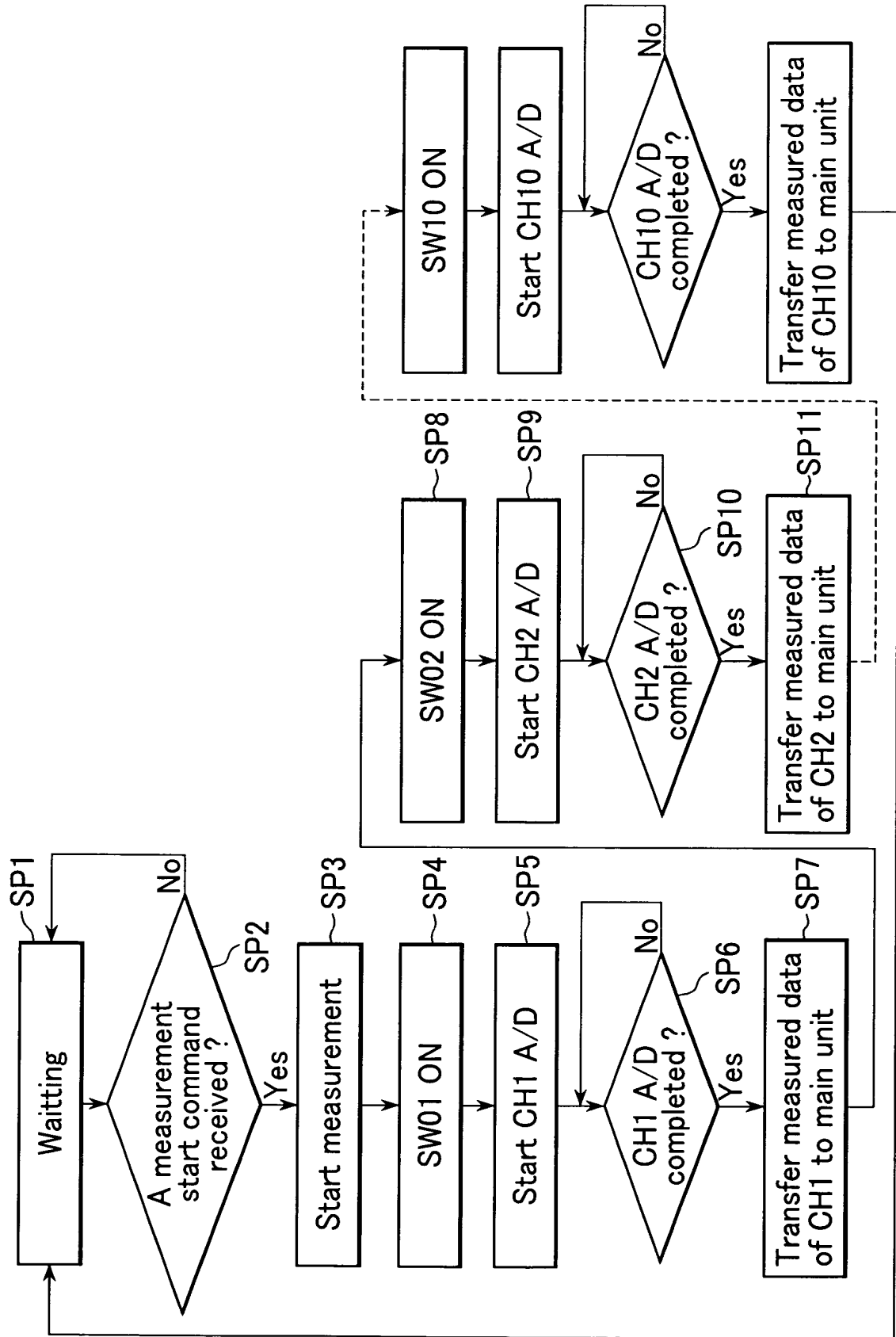
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FIG.6



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FIG. 7



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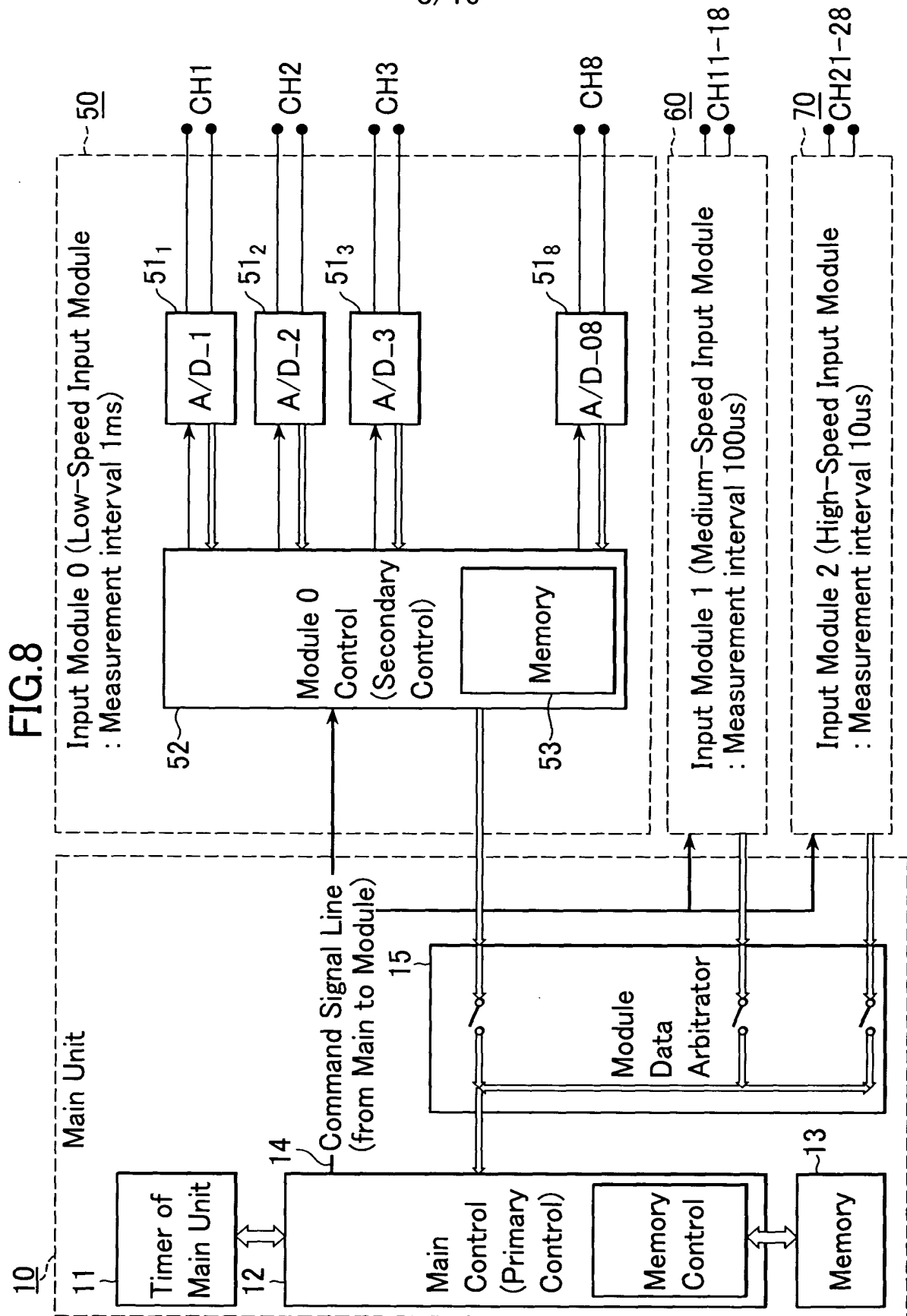
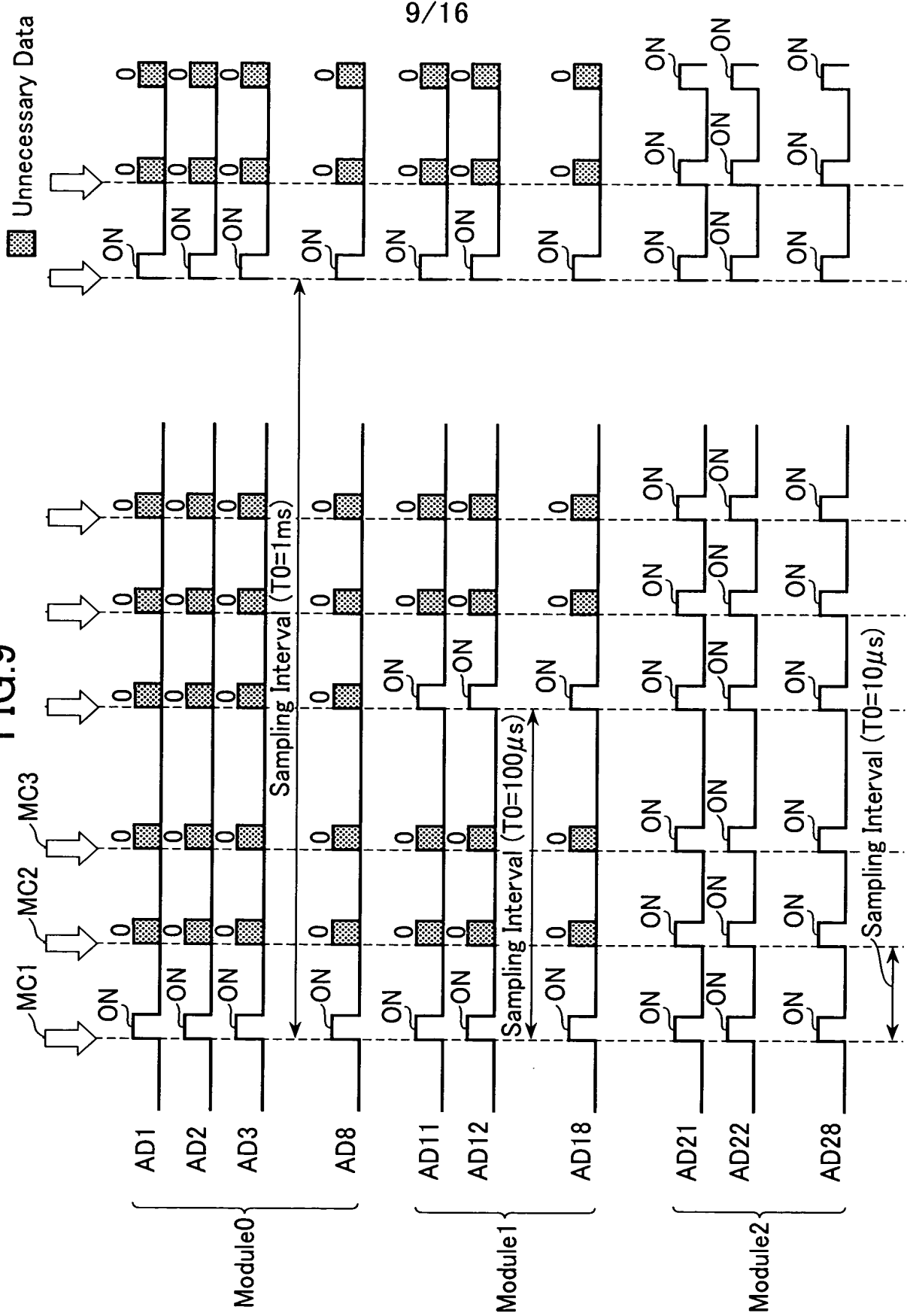


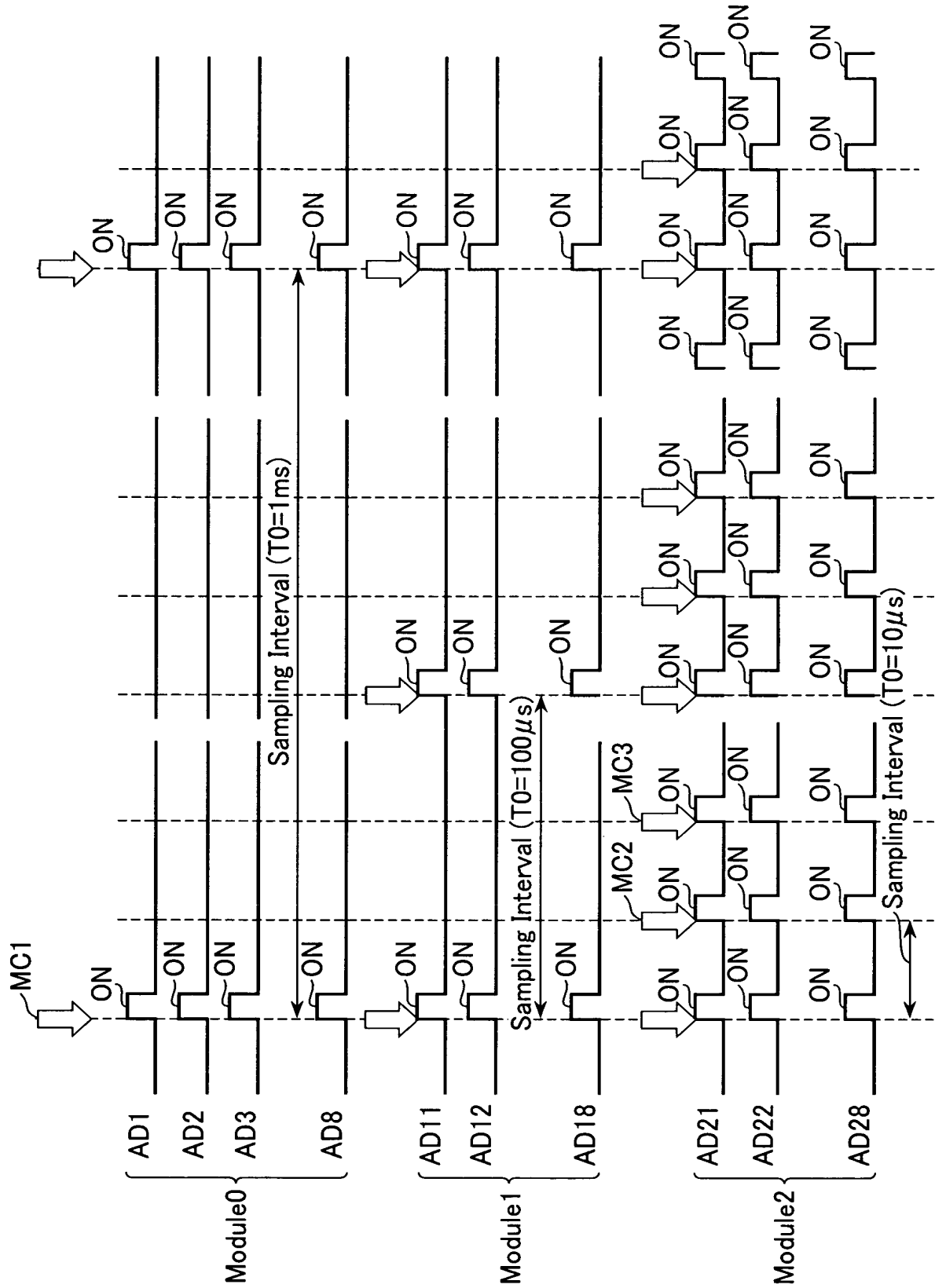


FIG. 9



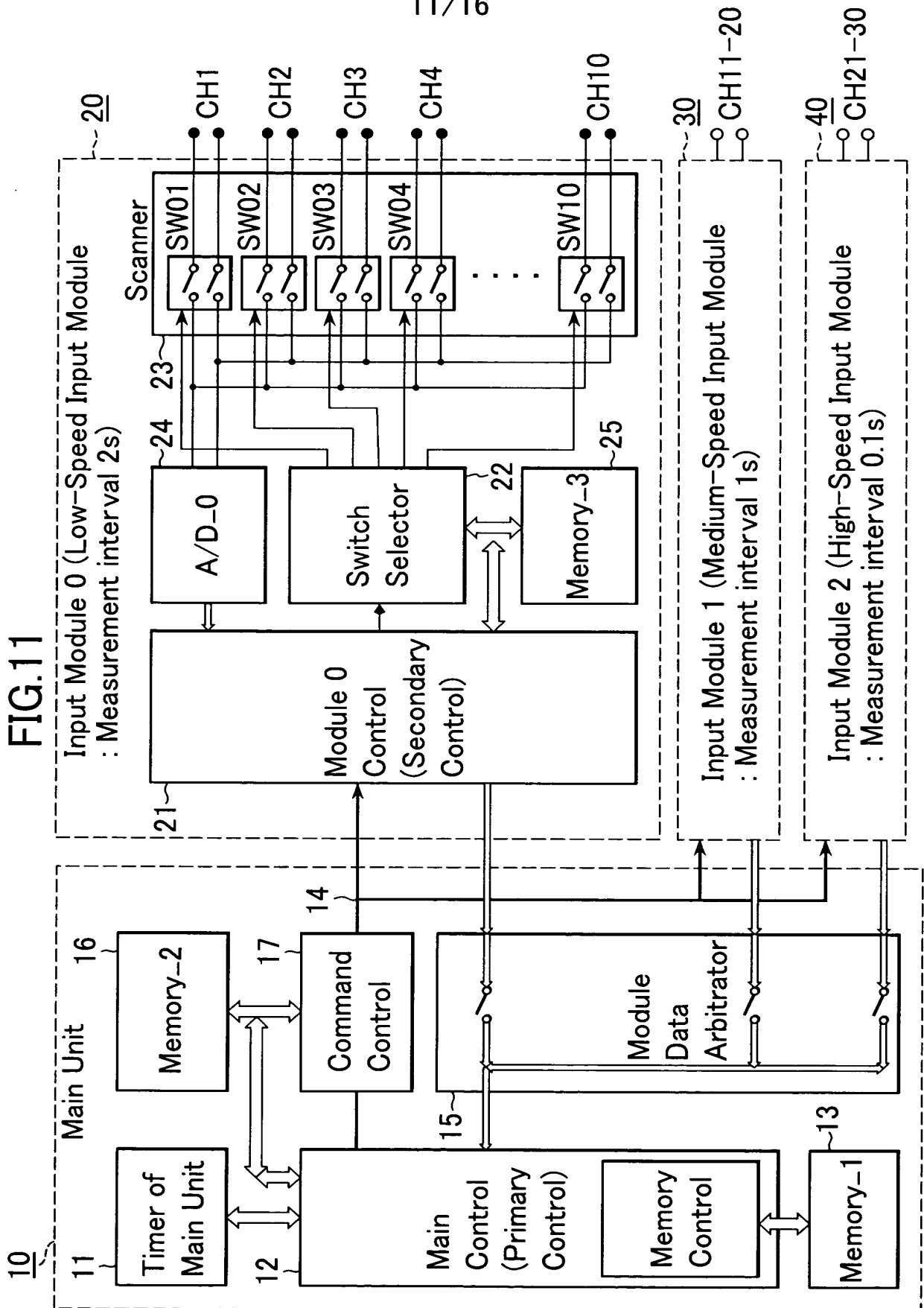
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FIG.10



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FIG.11



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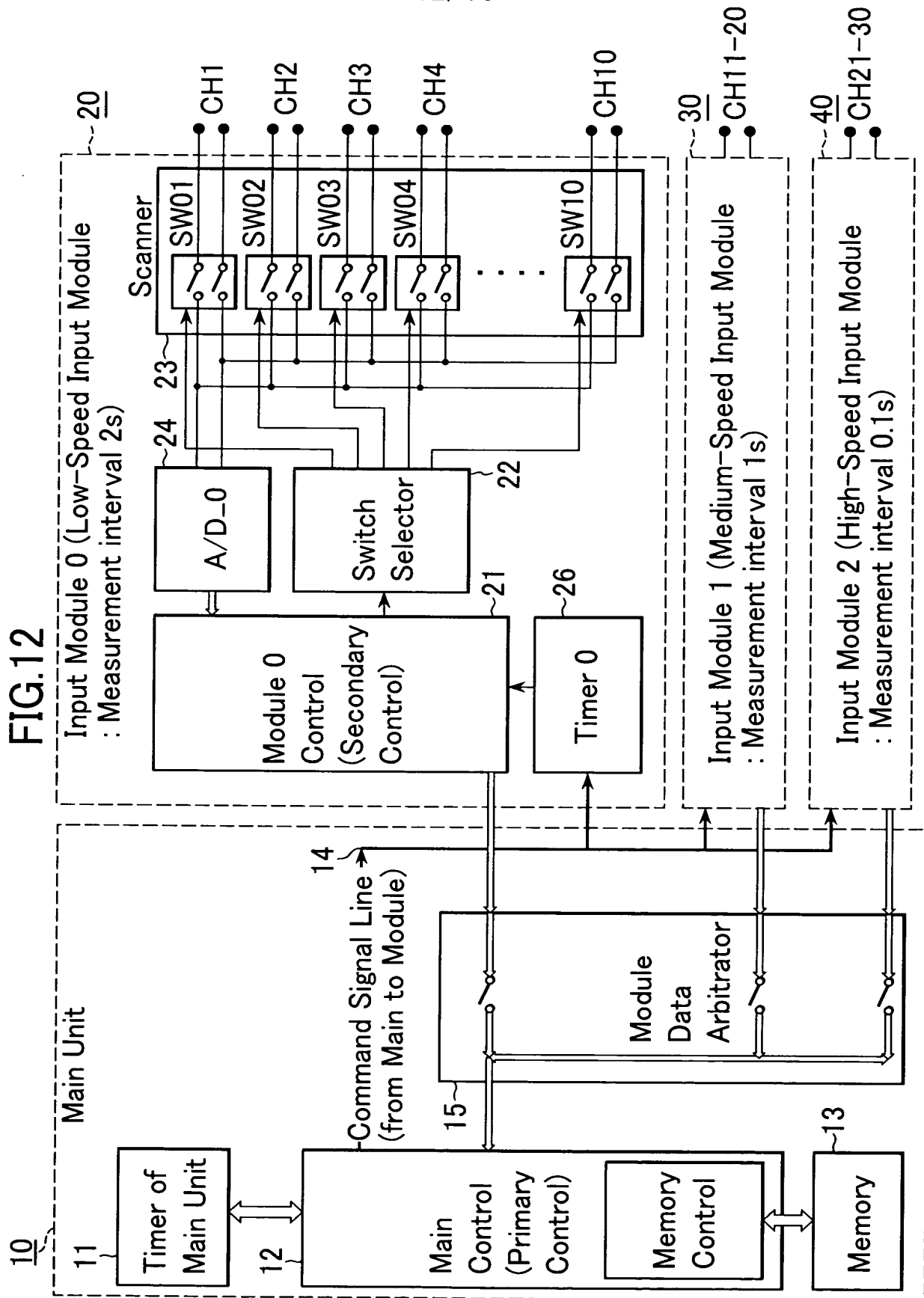
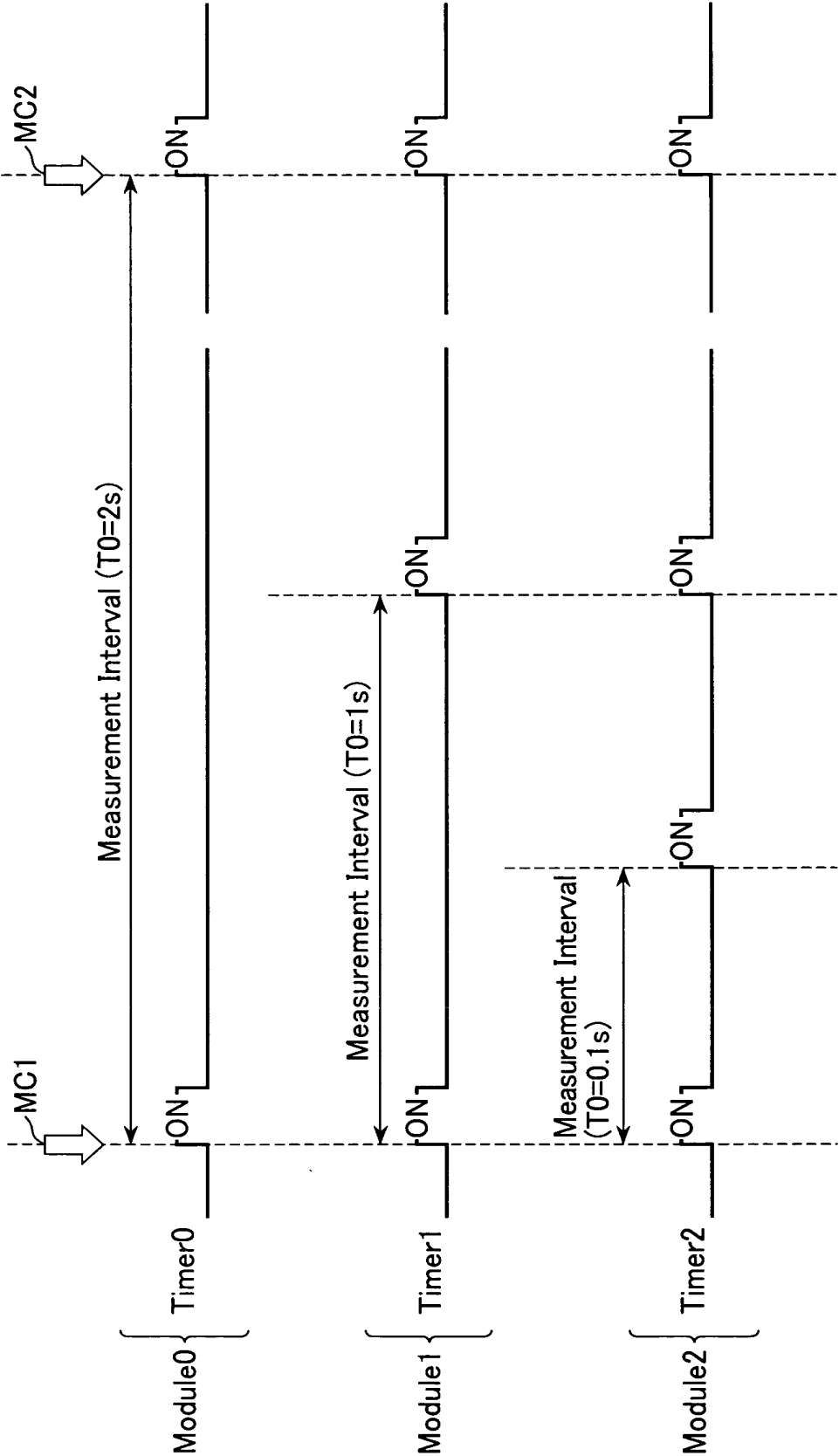
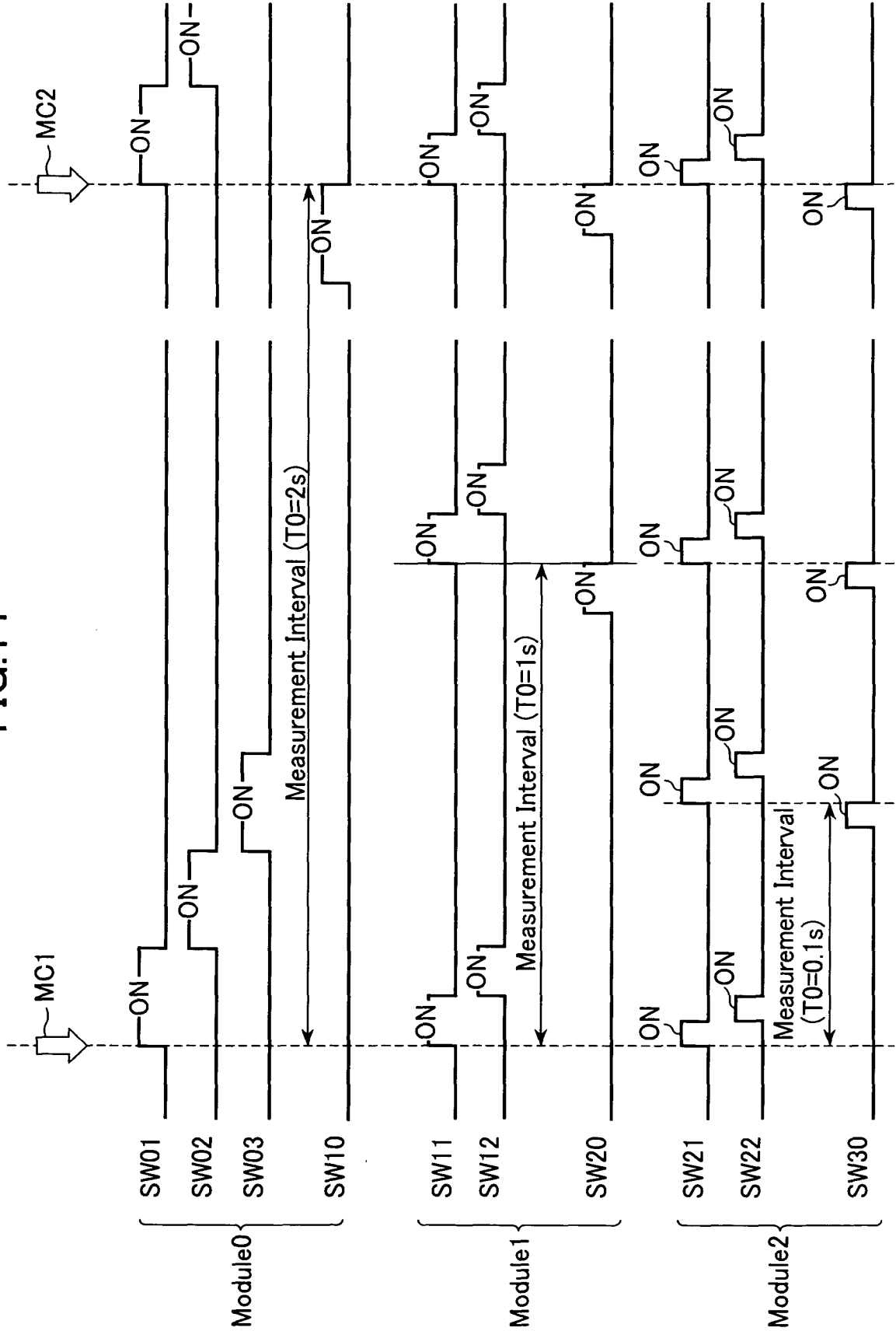


FIG.13



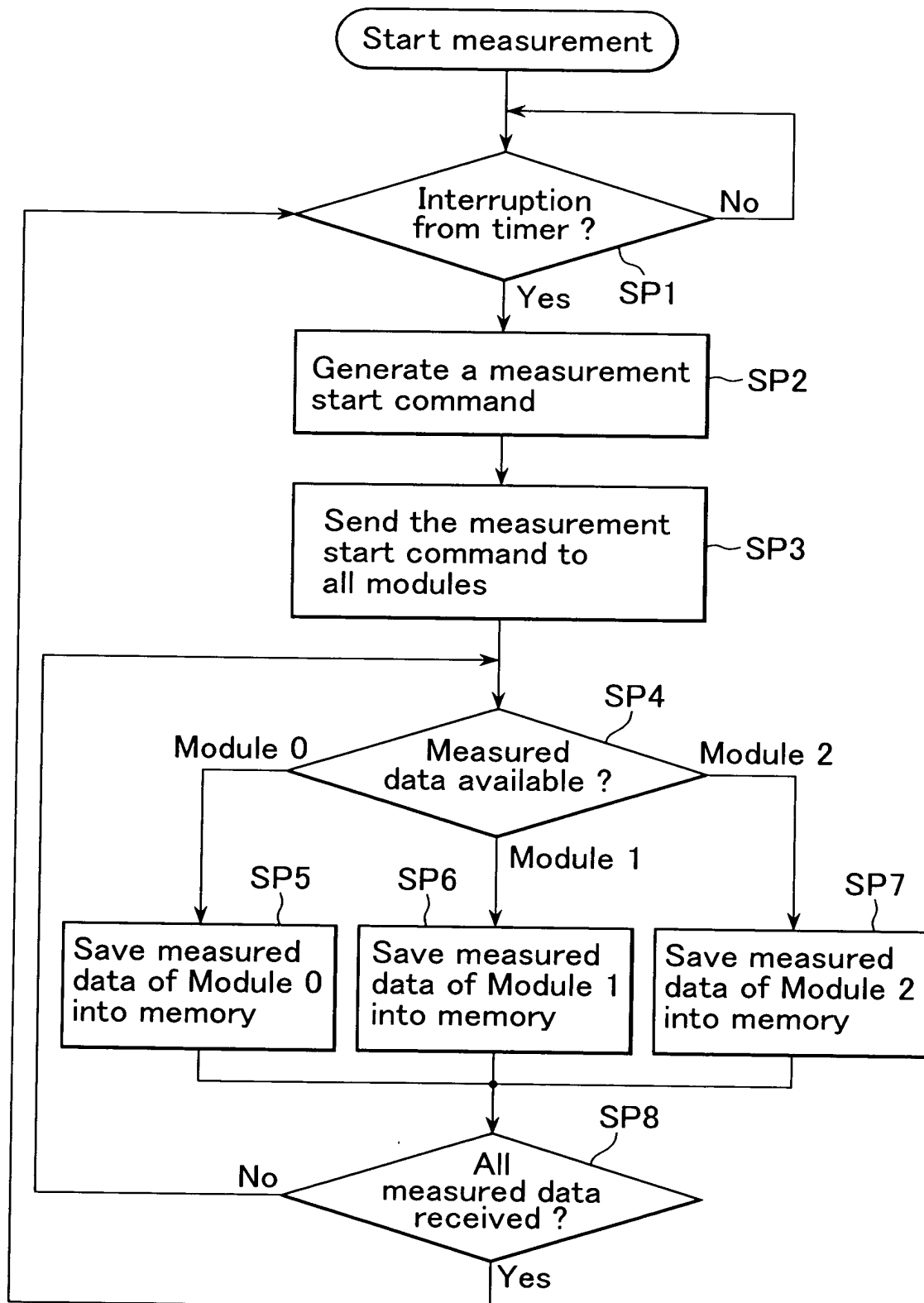
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FIG.14



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FIG.15



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FIG. 16

